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APPLICANT(S): GINZBURG, Boris et al.  
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### AMENDMENTS TO THE CLAIMS

Please add or amend the claims to read as follows, and cancel without prejudice or disclaimer to resubmission in a divisional or continuation application claims indicated as cancelled:

#### Listing of Claims

1. (Currently Amended) A method comprising:

for each of a set of data packets to be transmitted from transmitting by a wireless communication device to an access point, determining the priority of the during an awake mode of said wireless communication device one or more data packets packet as being high priority or not high priority sent for transmission during a power save mode of said wireless communication device;

scheduling packets determined to be high priority for transmission;

storing data packets determined to be not high priority in a buffer; and

transmitting the data packets determined to be not high priority upon the occurrence of a full buffer condition.

2. (Currently amended) The method of claim 1, further comprising storing in a buffer buffering said one or more data packets during [[said]] a power save mode of said wireless communication device.

3. (Currently amended) The method of claim [[2]] 40, wherein transmitting during an awake mode comprises transmitting said one or more packets in response to a wake-up trigger.

4. (Original) The method of claim 3, wherein said wake-up trigger relates to an aggregate anticipated transmission time of the one or more data packets.

5. (Original) The method of claim 3, wherein said wake-up trigger relates to an aggregate size of the one or more data packets.

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6. (Original) The method of claim 3, wherein said wake-up trigger relates to a period of time during which no data packets are sent for transmission.
7. (Cancelled)
8. (Cancelled)
9. (Cancelled)
10. (Currently amended) The method of claim [[1]] 40, wherein transmitting during an awake mode comprises transmitting an awake mode signal to indicate a start of said awake mode.
11. (Currently amended) The method of claim [[1]] 40, wherein transmitting during an awake mode comprises transmitting a power save signal to indicate an end of said awake mode.
12. (Currently amended) The method of claim [[1]] 2, comprising disabling a transmitter during said power save mode.
13. (Currently Amended) A program storage device having instructions readable by a machine that when executed by the machine result in:

for each of a set of data packets to be transmitted from transmitting by a wireless communication device to an access point, determining the priority of the  
during an awake mode of said wireless communication device one or more data  
packets packet as being high priority or not high priority sent for transmission during  
a power save mode of said wireless communication device; and  
scheduling packets determined to be high priority for transmission;  
storing data packets determined to be not high priority in a buffer;

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transmitting the data packets determined to be not high priority upon the occurrence of a full buffer condition.

14. (Currently amended) The program storage device of claim 13, wherein said instructions further result in storing in a buffer buffering said one or more data packets during [[said]]a power save mode of said wireless communication device.
15. (Currently amended) The program storage device of claim [[14]]41, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting said one or more packets in response to a wake-up trigger.
16. (Original) The program storage device of claim 15, wherein said wake-up trigger relates to an aggregate anticipated transmission time of the one or more data packets.
17. (Original) The program storage device of claim 15, wherein said wake-up trigger relates to an aggregate size of the one or more data packets.
18. (Original) The program storage device of claim 15, wherein said wake-up trigger relates to a period of time during which no data packets are sent for transmission.
19. (Cancelled)
20. (Cancelled)
21. (Cancelled)
22. (Currently Amended) The program storage device of claim [[13]]41, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting an awake mode signal to indicate a start of said awake mode.

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23. (Currently Amended) The program storage device of claim [[13]] 41, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting a power save signal to indicate an end of said awake mode.
24. (Currently Amended) The program storage device of claim [[13]] 14, wherein the instructions result in disabling a transmitter during said power save mode.
25. (Currently Amended) An apparatus comprising:
  - a buffer to store one or more data packets determined to be not high priority during a power save mode of said apparatus; and
  - a transmitter operatively coupled to said buffer, said transmitter to transmit during an awake mode of said apparatus said one or more data packets determined to be high priority and, upon the occurrence of a full buffer condition, data packets determined to be not high priority stored by said buffer during said power save mode of said apparatus.
26. (Currently Amended) The apparatus of claim 25, further comprising a processor adapted to transmit an awake signal to indicate a start of [[said]] an awake mode.
27. (Original) The apparatus of claim 26, wherein said processor is further adapted to transmit a power save signal to indicate an end of said awake mode.
28. (Original) The apparatus of claim 27, comprising a disabling unit to disable said transmitter during said power save mode.
29. (Original) The apparatus of claim 28, wherein said disabling unit is able to enable said transmitter during said power save mode.
30. (Currently Amended) A wireless communication device comprising:
  - a buffer to store one or more data packets determined to be not high priority during a power save mode of said apparatus; and

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a transmitter operatively coupled to said buffer, said transmitter to transmit during an awake mode of said wireless communication device said one or more data packets determined to be high priority and, upon the occurrence of a full buffer condition, data packets determined to be not high priority stored by said buffer during said power save mode of said wireless communication device; and

an omni-directional antenna operationally coupled to said transmitter.

31. (Original) The wireless communication device of claim 30, further comprising a processor to produce said one or more data packets.
32. (Currently Amended) The wireless communication device of claim [[31]] 30, wherein said transmitter is further adapted to transmit an awake mode signal to indicate a start of [[said]] an awake mode.
33. (Currently Amended) The wireless communication device of claim [[31]] 32, wherein said transmitter is further adapted to transmit a power save mode signal to indicate an end of said awake mode.
34. (Currently Amended) The wireless communication device of claim [[31]] 32, further comprising a power source and circuitry to connect said transmitter to said power source during said awake mode.
35. (Previously Presented) The wireless communication device of claim 34, further comprising circuitry to disconnect said transmitter from said power source during a power save mode.
36. (Currently Amended) A wireless communication system comprising:  
a first wireless communication device adapted to:  
~~determine the priority of each of a set of data packets to be transmitted from transmit during an awake mode of said first wireless communication device to an access point, as being high priority or not high priority one or more data packets sent~~

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~~for transmission by said first wireless communication device during a power save mode of said first wireless communication device;~~

schedulc packets determined to be high priority for transmission;

storc data packets determined to be not high priority in a buffer; and

transmit the data packets determined to be not high priority upon the occurrence of a full buffer condition; and

a second wireless device adapted to receive said one or more data packets.

37. (Currently Amended) The wireless communication system of claim 36, wherein said second wireless device is further adapted to transmit during [[said]] an awake mode one or more data packets sent for transmission during [[said]] a power save mode.
38. (Original) The wireless communication system of claim 37, wherein said first wireless device is further adapted to transmit an awake mode signal to indicate a start of an said awake mode.
39. (Previously Presented) The wireless communication system of claim 38, wherein said first wireless device is further adapted to transmit a power save mode signal to indicate an end of said awake mode.
40. (New) The method of claim 1, wherein transmitting occurs during an awake mode of said wireless communication device.
41. (New) The program storage device of claim 13, wherein the instructions result in transmitting during an awake mode of said wireless communication device.